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Substitute for form 1449-PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

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**Complete if Known**

Application Number	09/898583
Filing Date	7/3/2001
First Named Inventor	G. Glenn Henry
Art Unit	2183
Examiner Name	HENRY TSAI
Attorney Docket Number	CNTR.2020

Sheet	1	of	4
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Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
125		5706491	01-06-1998	Steven C. McMahan	
		5313634	05-17-1994	James Eickemeyer	
		5961629	10-05-1999	Nguyen et al	
		5832289	11-03-1998	Shaw et al	
		5828901	10-27-1998	O'Toole et al	
		5864707	01-26-1999	Tran et al	
		6233676	05-15-2001	Henry et al	
		5623615	04-22-1997	Salem et al	
		6175897	01-16-2001	Ryan et al	
		5734881	03-31-1998	White et al	
		6260138	07-10-2001	Jeremy G. Harris	
		6823444	11-23-2004	Henry et al	
		6256727	07-03-2001	Robert Greg McDonald	
		5931944	08-03-1999	Ginosar et al	
		5881260	03-09-1999	Raje et al	
		4860197	08-22-1989	Langendorf et al	
		5142634	08-25-1992	Fite et al	
		5163140	11-10-1992	Stiles et al	
		5404467	04-04-1995	Saba et al	
		5687360	11-11-1997	Po-Hua Chang	
		5761723	06-02-1998	Black et al	
		5867701	02-02-1999	Brown et al	
		5964868	10-12-1999	Gochman et al	
		5974543	10-26-1999	Hilgendorf et al	
		5978909	11-02-1999	Oded Lempel	

Examiner Signature	<i>Henry Tsai</i>	Date Considered	9/20/05
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125		6088793	07-11-2000	Liu et al	
		6151671	11-21-2000	D'Sa et al	
		6314514	11-06-2001	Thomas C. McDonald	
		6502185	12-31-2002	Keller et al	
		6601161	07-29-2003	Rappoport et al	
		6647467	11-11-2003	Eric M. Dowling	
		5355459	10-11-1994	Matsuo et al	
		6108773	08-22-2000	Col et al	
		5513330	04-30-1996	David R. Stiles	
		6457120	09-24-2002	Balaram Sinharoy	
		5634103	05-27-1997	Dietz et al	
		6044459	03-28-2000	Bae et al	
		5530825	06-25-1996	Black et al	
		5812839	09-22-1998	Hoyt et al	
		5604877	02-18-1997	Hoyt et al	
		6134654	10-17-2000	Patel et al	
		4181942	01-01-1980	Forster et al	
		5687349	11-11-1997	Ralph C. McGarity	
		6279105	08-21-2001	Konigsburg et al	
	229		5881265	03-09-1999	McFarland et al
		5968169	10-19-1999	James K. Pickett	
		6085311	07-04-2000	Narayan et al	
		6308259	10-23-2001	David B. Witt	
		5623614	04-22-1997	Van Dyke et al	
		5768576	06-16-1998	Hoyt et al	

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15		6170054	01-02-2001	Mitchell Alexander Poplingher	
		6279106	08-21-2001	James S. Roberts	
		2002194464	12-19-2002	Henry et al	
		5721855	02-24-1998	Hinton et al	
		4200927	04-29-1980	Hughes et al	
		6185676	02-06-2001	Poplingher et al.	
		5394530	02-28-1995	Mayumi Kita	
		6886093	04-26-2005	G. Glenn Henry	
		20040030866	02-12-2004	Thomas C. McDonald	
		20020194461	12-19-2002	Henry et al.	
		20020194460	12-19-2002	Henry et al.	
		20050132175	06-16-2004	Henry et al.	
		20050044343	02-24-2005	Henry et al.	
		20020188833	12-12-2002	Henry et al.	
		20050114636	05-26-2005	McDonald et al.	
		20040139281	07-15-2004	Thomas C. McDonald	
		20040139301	07-15-2004	Thomas C. McDonald	
		20040143727	07-22-2004	Thomas C. McDonald	
		20040143709	07-22-2004	Thomas C. McDonald	
		20040139292	07-15-2004	Thomas C. McDonald	
		20050076193	04-07-2005	Henry et al.	

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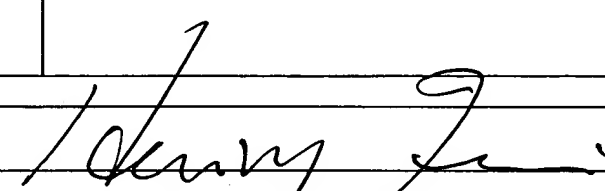
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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite. No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
125	AA	YEH ET AL. <i>Alternative Implementation of Two-Level Adaptive Branch Prediction</i> . 19th Annual International Symposium on Computer Architecture. Pp124-134. May 19-21, 1992. Gold Coast, Australia	
	AB	CHANG ET AL. <i>Alternative Implementations of Hybrid Branch Predictors</i> . Proceedings of MICRO-28. 1995. IEEE.	
	AC	MC FARLING, SCOTT. <i>WRL Technical Note TN-36. "Combining Branch Predictors</i> . June 1993. Western Research Laboratory, 250 University Ave., Palo Alto, CA 94301	
	AD	BRAY ET AL. <i>Strategies For Branch Target Buffers</i> . Technical Report No. CSL-TR-91-480. June 1991.	
125	AE	SAKAMOTO ET AL. <i>Microarchitecture Support for Reducing Branch Penalty in a Superscaler Processor</i> . Pp 208-216. Mitsubishi Electric Corp. 4-1 Mizuhara, Itami, Hyogo 664. Japan, 1996. IEEE.	

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